IN THE CLAIMS

Please amend claims 1, 9, 10, 20, 28 and 29 as follows:

--1(amended). A signal processor comprising:

- a data compression encoder which operates to compression encode source data into compression encoded data having a variable compressed data rate,

- a multiplexer coupled to said data compression encoder and arranged in operation to concatenate said compressed data and ancillary data into a concatenated data stream, and

- a control processor coupled to said multiplexer and arranged in operation to control said multiplexer whereby a combined data rate of said concatenated data stream is less than or equal to a pre-determined maximum;

whereby said ancillary data is concatenated with compressed data corresponding to two or more non-contiguous frames of video data.--

--9(amended). A signal processor as claimed in Claim 8, wherein said encoded representation of said source data is formed using a discrete cosine transform.--

--10(amended). A signal processor as claimed in Claim 9, wherein said compression encoding algorithm is an MPEG type algorithm.--

--20(amended). A method of processing source data comprising the steps of

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